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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Shinsuke SADAMITSU, et al

SERIAL NUMBER: 10/519,837

FILED: January 11, 2005

FOR: HIGH-RESISTANCE SILICON WAFER AND ITS MANUFACTURING METHOD

RESPONSE TO NOTICE OF MISSING REQUIREMENTS UNDER 35 U.S.C. 371

COMMISSIONER FOR PATENTS

Box PCT

Alexandria, VA 22313

SIR:

Responsive to the notification dated June 14, 2005, and in accordance with the provisions of 35 U.S.C. 371, Applicants submit herewith a Rule 63 Declaration

In light of the foregoing, this application is deemed to be in proper condition for examination and such favorable action is earnestly solicited.

Our payment in the amount of \$-0.00- is attached hereto. If any variance exists between the amount enclosed and the required Government fee, please charge or credit the difference to our Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136 and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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